

Ap 28/11
PATENT

U.S. PATENT AND TRADEMARK OFFICE

In re application of: KELKAR et al.

Application No.: 09/653,925

Filed: September 1, 2000

Title: WAFER LEVEL CHIP SCALE PACKAGE

Attorney Docket No.: TC 2000 MAIL ROOM
NSC1P181/P04767

Examiner: Parekh, N.

Group: 2811

Feb 7 2002

5/Election
Chairman
2-16-02



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail to: Commissioner for Patents, Washington, D.C. 20231 on November 14, 2001.

Signed: Sue Funchess
Sue Funchess

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

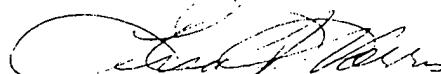
Dear Sir:

BEST AVAILABLE COPY

In response to the restriction requirement imposed by the Examiner in the Office Action dated November 5, 2001, Applicants respectfully elect to prosecute Invention I, namely, claims 1-7 and 15-20, drawn to a semiconductor device.

Applicants believe that no fees are due in connection with the filing of this Response. However, if any fees are due, including fees for a petition for extension of time, the Commissioner is authorized to charge such fees or credit any overpayment to our Deposit Account No. 50-0388 (Order No. NSC1P181).

Respectfully submitted,
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